

**Preliminary Amendment**

Applicant: Rao Rajashekhar et al.

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Title: DEVICE FOR UTILIZATION WITH THE SYNCHRONIZATION OF CLOCK SIGNALS, AND CLOCK SIGNAL SYNCHRONIZING METHOD (As Amended)

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**IN THE ABSTRACT**

Please replace the Abstract with the following rewritten paragraph:

**DEVICE FOR UTILIZATION WITH THE SYNCHRONIZATION OF CLOCK SIGNALS, AS WELL AS CLOCK SIGNAL SYNCHRONIZING METHOD**

**Abstract**

~~The invention relates to~~ A clock signal synchronizing method and ~~to~~ an apparatus device (1) for utilization with the synchronization of clock signals ~~(CLK)~~ is disclosed. In one embodiment, the apparatus includes a, comprising delay device (2) with a variably controllable delay time ( $t_{var}$ ) into which a clock signal ~~(CLK)~~, or a signal obtained therefrom, is input, charged with the variably controllable delay time ~~( $t_{var}$ )~~, and output as a delayed clock signal ~~(DQS)~~.

~~characterized in that~~

a device (5) is provided for determining whether a clock edge (~~A'~~) of the delayed clock signal ~~(DQS)~~ output by the delay device (2), or of a signal ~~(FB)~~ obtained therefrom, lies within a predetermined time window before a corresponding clock edge (~~A~~) of the clock signal ~~(CLK)~~, or of the signal obtained therefrom.

~~Figure 3~~